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Docket No.: M4065.0100/P100-B
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:
Howard E. Rhodes

Patent No.: 6,930,337

Issued: August 16, 2005

For: CMOS IMAGER WITH SELECTIVELY
SILICIDED GATES

SECOND REQUEST FOR REISSUED CERTIFICATE OF CORRECTION

Commissioner for Patents
Office of Patent Publication
Attention: Certificate of Correction Branch
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate

NOV 04 2008

of Correction

Dear Sir:

Having received neither a Reissued Certificate of Correction in response to the request filed on January 27, 2006 nor any response from the USPTO despite numerous inquiries regarding its status, as detailed below and evidenced by the attached exhibits, Patantee hereby submits a Second Request for Reissued Certificate of Correction.

U.S. Patent No. 6,930,337 issued on August 16, 2005. On September 26, 2005, Patantee filed a Request for Certification of Correction along with the \$100.00 fee set forth in 37 C.F.R. § 1.20(a). See Exhibit A.

On November 15, 2005, the USPTO issued a Certificate of Correction in response to Patantee's request. See Exhibit B. Upon review, Patantee noticed that the Certificate of Correction omitted numerous corrections included in Patantee's request. More specifically, all

of the corrections listed on the second page of Form PTO/SB/44 were omitted. Compare Exhibit A at pages 5 and 6 with Exhibit B.

Patantee submitted a [First] Request for Reissued Certificate of Correction on January 27, 2006. See Exhibit C. The request explained that “page 2 was omitted” and included Patantee’s original Request for Certificate of Correction and the incomplete Certificate of Correction as exhibits thereto. An itemized postcard stamped by OIPE confirms that the USPTO received Patantee’s [First] Request for Reissued Certificate of Correction on January 27, 2006. See Exhibit C at page 2.

After nearly seven months passed without a reply from the USPTO, Patantee submitted a Status Inquiry on August 14, 2006. See Exhibit D.

Following another four months without any response from the USPTO, Patantee submitted a second Status Inquiry on December 15, 2006. See Exhibit E. Again, Patantee received no response.

On February 14, 2008, more than two years after submitting its [First] Request for Reissued Certificate of Correction and still having received no response whatsoever from the USPTO, Patantee telephoned the Certificates of Correction Branch to inquire regarding the status of its request. Patantee left a detailed voicemail including the patent number and contact information, but Patantee’s call was never returned.

Several months later and still having received no response whatsoever from the USPTO despite two written Status Inquiries and a voicemail left at the Certificates of Correction Branch, Patantee initiated a series of telephone calls to the main USPTO Customer Service Center. Contemporaneous notes taken by Patantee’s representative memorialize calls on July 17, 2008 and July 31, 2008 and August 5, 2008. See Exhibit F.

As the notes indicate, Patantee’s telephone call on July 17, 2008 was assigned reference number 1125499649. Patantee advised the customer service representative that voicemails left for the Certificates of Correction Branch had not been returned. The customer service

representative agreed to escalate the issue to a supervisor and assured Patantee of a return phone call. See Exhibit F at page 1.

Having not received the promised return phone call, Patantee telephoned the USPTO Customer Service Center again on July 31, 2008. Patantee was again assured that the issue would be escalated to a supervisor and promised a return phone call within 48 hours. See Exhibit F at page 2.

After five days passed with no response from the USPTO, Patantee telephoned the Customer Service Center yet again on August 5, 2008. Upon recounting the entire saga, Patantee was politely informed that there was nothing more the customer service representative could do and that Patantee would receive a return phone call "eventually." See Exhibit F at page 3.

Today, more than three years after first filing its Request for Certificate of Correction, Patantee submits this Second Request for Reissued Certificate of Correction. Please issue a Certificate of Correction containing all of the corrections requested in Patantee's original request. See Exhibit A at pages 5 and 6.

If the USPTO has any questions concerning this request, please telephone Patantee's undersigned representative.

Dated: October ³¹~~29~~, 2008

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371
DICKSTEIN SHAPIRO LLP
1825 Eye Street NW
Washington, DC 20006
(202) 420-2200
Attorney for Applicant

EXHIBIT

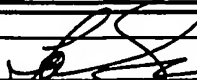
A

Under the Paperwork Reduction Act of 1995, no person are required to respond to a collection of information unless it displays a valid OMB control number.

Effective on 12/08/2004. Fees pursuant to the Consolidated Appropriations Act, 2005 (H.R. 4818). FEE TRANSMITTAL For FY 2005		Complete if Known	
<input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27		Application Number	Patent#: 6,930,337
		Filing Date	Issued: August 16, 2005
		First Named Inventor	Howard E. Rhodes
		Examiner Name	T. N. Quach
		Art Unit	2814
TOTAL AMOUNT OF PAYMENT		(\$)	100.00
		Attorney Docket No.	M4065.0100/P100-B

METHOD OF PAYMENT (check all that apply)	
<input type="checkbox"/> Check <input checked="" type="checkbox"/> Deposit Account	<input checked="" type="checkbox"/> Credit Card Deposit Account Number: <u>04-1073</u> Deposit Account Name: <u>Dickstein Shapiro Morin & Oshinsky LLP</u>
For the above-identified deposit account, the Director is hereby authorized to: (check all that apply)	
<input type="checkbox"/> Charge fee(s) indicated below <input type="checkbox"/> Charge any additional fee(s) or underpayment of fee(s) under 37 CFR 1.16 and 1.17	<input type="checkbox"/> Charge fee(s) indicated below, except for the filing fee <input checked="" type="checkbox"/> Credit any overpayments

FEE CALCULATION							
1. BASIC FILING, SEARCH, AND EXAMINATION FEES							
Application Type	FILING FEES		SEARCH FEES		EXAMINATION FEES		Fees Paid (\$)
	Fee (\$)	Small Entity Fee (\$)	Fee (\$)	Small Entity Fee (\$)	Fee (\$)	Small Entity Fee (\$)	
Utility	300	150	500	250	200	100	
Design	200	100	100	50	130	65	
Plant	200	100	300	150	160	80	
Reissue	300	150	500	250	600	300	
Provisional	200	100	0	0	0	0	
2. EXCESS CLAIM FEES							
Fee Description						Fee (\$)	Small Entity Fee (\$)
Each claim over 20 (including Reissues)						50	25
Each independent claim over 3 (including Reissues)						200	100
Multiple dependent claims						360	180
Total Claims		Extra Claims	Fee (\$)	Fee Paid (\$)	Multiple Dependent Claims		
_____		_____	_____	_____	Fee (\$) Fee Paid (\$)		
Indep. Claims		Extra Claims	Fee (\$)	Fee Paid (\$)			
_____		_____	_____	_____			
3. APPLICATION SIZE FEE							
If the specification and drawings exceed 100 sheets of paper (excluding electronically filed sequence or computer listings under 37 CFR 1.52(e)), the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).							
Total Sheets	Extra Sheets	Number of each additional 50 or fraction thereof		Fee (\$)	Fee Paid (\$)		
_____	_____	_____ /50 _____ (round up to a whole number) x		_____	_____		
4. OTHER FEE(S)							
Non-English Specification, \$130 fee (no small entity discount)							
Other (e.g., late filing surcharge): 1811 Certificate of correction						100.00	

SUBMITTED BY			
Signature		Registration No. (Attorney/Agent)	28,371
Name (Print/Type)	Thomas J. D'Amico	Telephone	(202) 828-2232
		Date	September 26, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:
Howard E. Rhodes

Patent No.: 6,930,337

Issued: August 16, 2005

For: CMOS IMAGER WITH SELECTIVELY
SILICIDED GATES

REQUEST FOR CERTIFICATE OF CORRECTION
PURSUANT TO 37 CFR 1.322 & 1.323

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Upon reviewing the above-identified patent, Patentee noted typographical errors which should be corrected.

Errors in the Specification that were made by the PTO are:

Column 2, line 49, "ate" should read --are--;

Column 4, line 34, "diff-diffusion" should read --diffusion--; and

Column 4, line 52, "fine" should read --line--.

Errors in the Specification that appear in the application are:

Column 10, line 20, "be formed using replaced" should read --be replaced--;

Column 10, line 65, "antimony of" should read --antimony or--;

Column 11, line 33, "be formed using replaced" should read --be replaced--;

and

Column 12, line 5, "antimony of" should read --antimony or--.

In the Claims errors made by the PTO are:

Claim 2, column 13, line 21, "suicide" should read --silicide--;

Claim 3, column 13, line 24, "suicide" should read --silicide--;

Claim 4, column 13, line 26, "suicide" should read --silicide--;

Claim 5, column 13, line 28, "suicide" should read --silicide--;

Claim 6, column 13, line 30, "suicide layer" should read --silicide layer--;

Claim 6, column 13, line 30, "suicide." Should read --silicide.--;

Claim 7, column 13, line 31, "claim 2" should read --claim 1--;

Claim 18, column 14, line 12, "suicide" should read --silicide--;

Claim 20, column 14, line 16, "suicide" should read --silicide--;

Claim 36, column 16, line 4, "suicide layer" should read --silicide layer--;

Claim 36, column 16, line 4, "suicide." Should read --silicide.--; and

Claim 37, column 16, line 6, "suicide" should read --silicide--.

The errors were made primarily by the PTO although some were found in the application as filed by applicant. Please charge our Credit Card in the amount of \$100.00

covering the fee set forth in 37 CFR 1.20(a). Credit Card Payment Form SB-2038, with a signature from an authorized cardholder, is enclosed.

The errors now sought to be corrected are inadvertent typographical errors the correction of which does not involve new matter or require reexamination.

Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentee respectfully solicits the granting of the requested Certificate of Correction.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0100/P100-B.

Dated: September 26, 2005

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Elizabeth Parsons

Registration No.: 52,499

DICKSTEIN SHAPIRO MORIN &
OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicant

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 6,930,337
APPLICATION NO. : 10/617,706
ISSUE DATE : August 16, 2005
INVENTOR(S) : Howard E. Rhodes

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Errors in the Specification are:

Column 2, line 49, "ate" should read --are--;

Column 4, line 34, "diff-diffusion" should read --diffusion--; and

Column 4, line 52, "fine" should read --line--.

Column 10, line 20, "be formed using replaced" should read --be replaced--;

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Claim 4, column 13, line 26, "suicide" should read --silicide--;

Claim 5, column 13, line 28, "suicide" should read --silicide--;

MAILING ADDRESS OF SENDER (Please do not use customer number below):
Thomas J. D'Amico
DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP 1
2101 L Street NW
Washington, DC 20037-1526

Claim 6, column 13, line 30, "suicide layer" should read --silicide layer--;

Claim 6, column 13, line 30, "suicide." Should read --silicide.--;

Claim 7, column 13, line 31, "claim 2" should read --claim 1--;

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Claim 36, column 16, line 4, "suicide." Should read --silicide.--; and

Claim 37, column 16, line 6, "suicide" should read --silicide--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Thomas J. D'Amico

DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP 2

2101 L Street NW

Washington, DC 20037-1526

Inventor: Howard E. Rhodes

Atty Docket No.: M4065.0100/P100-B

Patent No.: 6,930,337

Issue Date: August 16, 2005

Title: CMOS IMAGER WITH SELECTIVELY SILICIDED GATES

Documents Filed:

Fee Transmittal (1 page)

Request for Certificate of Correction (Fee Required) (3 pages)

Certificate of Correction (2 pages)

Payment by credit card. Form PTO-2038 is attached (1 page)

Charge \$100.00 to credit card



Via: PTO Daily Run

Sender's Initials: TJD/EP/grb

Date: September 26, 2005

T.W.

EXHIBIT

B

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,930,337 B2
DATED : August 16, 2005
INVENTOR(S) : Howard E. Rhodes

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 2,
Line 49, "ate" should read -- are --;

Column 4,
Line 34, "diff-diffusion" should read -- diffusion --;
Line 52, "fine" should read -- line --;

Column 10,
Line 20, "be formed using replaced" should read -- be replaced --;
Line 65, "antimony of" should read -- antimony or --;

Column 11,
Line 33, "be formed using replaced" should read -- be replaced --;

Column 12,
Line 5, "antimony of" should read -- antimony or --;

Column 13,
Lines 21, 24, 26 and 28, "suicide" should read -- silicide --.



Signed and Sealed this
Fifteenth Day of November, 2005

JON W. DUDAS
Director of the United States Patent and Trademark Office

EXHIBIT

C

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:
Howard E. Rhodes

Patent No.: 6,930,337

Issued: August 16, 2005

For: CMOS IMAGER WITH SELECTIVELY
SILICIDED GATES

REQUEST FOR REISSUED CERTIFICATE OF CORRECTION

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450


Dear Sir:

The Certificate of Correction issued by the PTO on November 15, 2005 (copy attached as Exhibit A), is incomplete, as page 2 was omitted. A copy of Patentee's filing dated September 26, 2005 is attached along with a copy of the postcard showing that 2 pages of corrections were submitted (attached Exhibit B).

Patentee requests a reissued Certificate.

Dated: January ²⁷~~26~~, 2006

Respectfully submitted,

By 
Thomas J. D'Amico

Registration No.: 28,371
Elizabeth Parsons
Registration No.: 52,499
DICKSTEIN SHAPIRO MORIN & OSHINSKY
LLP
2101 L Street NW
Washington, DC 20037-1526
(202) 785-9700
Attorneys for Applicant

Inventor: Howard E. Rhodes

Atty Docket No.: M4065.0100/P100-B

Patent No.: 6,930,337

Issue Date: August 16, 2005

Title: CMOS IMAGER WITH SELECTIVELY SILICIDED GATES

Documents Filed:

Request for Reissued Certificate of Correction (No Fee) (10 pages)



M.B.
Via: PTO Daily Run

Sender's Initials: TJD/EP/grb

Date: January 27²⁷₂₈, 2006

Exhibit A

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,930,337 B2
DATED : August 16, 2005
INVENTOR(S) : Howard E. Rhodes

Page 1 of 1

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Column 13,

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Signed and Sealed this

Fifteenth Day of November, 2005

JON W. DUDAS

Director of the United States Patent and Trademark Office

Exhibit B

Atty Docket No.: M4065.0100/P100-B

Inventor: Howard E. Rhodes

Patent No.: 6,930,337

Issue Date: August 16, 2005

Title: CMOS IMAGER WITH SELECTIVELY SILICIDED GATES

Documents Filed:

Fee Transmittal (1 page)

Request for Certificate of Correction (Fee Required) (3 pages)

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Charge \$100.00 to credit card



Via: PTO Daily Run

Sender's Initials: TJD/EP/grb

Date: September 26, 2005

D.W.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:
Howard E. Rhodes

Patent No.: 6,930,337

Issued: August 16, 2005

For: CMOS IMAGER WITH SELECTIVELY
SILICIDED GATES

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PURSUANT TO 37 CFR 1.322 & 1.323**

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Alexandria, VA 22313-1450

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Dated: September 26, 2005

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Elizabeth Parsons

Registration No.: 52,499

DICKSTEIN SHAPIRO MORIN &
OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicant

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 6,930,337
APPLICATION NO. : 10/617,706
ISSUE DATE : August 16, 2005
INVENTOR(S) : Howard E. Rhodes

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MAILING ADDRESS OF SENDER (Please do not use customer number below):

Thomas J. D'Amico
DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP 1
2101 L Street NW
Washington, DC 20037-1526

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MAILING ADDRESS OF SENDER (Please do not use customer number below):

Thomas J. D'Amico

DICKSTEIN SHAPIRO MORIN & OSHINSKY LLP 2

2101 L Street NW

Washington, DC 20037-1526

EXHIBIT

D

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Howard E. Rhodes

Patent No.: 6,930,337

Issued: August 16, 2005

For: CMOS IMAGER WITH SELECTIVELY
SILICIDED GATES

STATUS INQUIRY

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

It is respectfully requested that the attorney named below be advised of the status of the above-identified application. Please advise us of when we might expect to receive a Reissued Certificate of Correction from the Patent and Trademark Office in response to our Request dated January 27, 2006.

Dated: August 14, 2006

Respectfully submitted,

By 

Thomas J. D'Amico
DICKSTEIN SHAPIRO LLP
Registration No.: 28,371
Elizabeth Parsons
Registration No.: 52,499
1825 Eye Street, NW
Washington, DC 20006-5403
(202) 420-2200
Attorney for Applicant

Attorney Docket No.: M4065.0100/P100-B

Inventor: Howard E. Rhodes

Patent No.: 6,930,337

Issue Date: August 16, 2005

Title: CMOS IMAGER WITH SELECTIVELY SILICIDED GATES

Documents Filed:

Status Inquiry for patent no. 6,930,337 (1 page)



Via: PTO Daily Run

Sender's Initials: TJD/EP/emb

Date: August 14, 2006

EW

EXHIBIT

E

Docket No.: M4065.0100/P100-B
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Howard E. Rhodes

Application No.: 10/617,706

Confirmation No.: 3719

Filed: July 14, 2003

Art Unit: 2814

For: CMOS IMAGER WITH SELECTIVELY
SILICIDED GATES

Examiner: T. N. Quach

STATUS INQUIRY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

It is respectfully requested that the attorney named below be advised of the status of the above-identified application. Please advise us of when we might expect to receive a Reissued Certificate of Correction from the Patent and Trademark Office in response to our Request dated January 27, 2006.

Dated: December 15, 2006

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371
DICKSTEIN SHAPIRO LLP
1825 Eye Street, NW
Washington, DC 20006-5403
(202) 420-2200
Attorney for Applicant

7

Atty Docket No.: M4065.0100/P100-B

Inventor: Howard E. Rhodes

Patent No.: 6,930,337

Issue Date: August 16, 2005

Title: CMOS IMAGER WITH SELECTIVELY SILICIDED GATES

Documents Filed:

Status Inquiry for application 10/617,706 (Reissued Cert. of Correction) (1 page)



dm

Via: PTO Courier

Sender's Initials: TJD/MJR/mjo/fet

Date: December 15, 2006

EXHIBIT

F

7/17/08

Telephoned USPTO
at 2:00 PM, again, again,
again... Ref # 1125499649

Voice-mails to CoL not
returned.

Customer service escalating
to supervisor. Will call
back.

7/31/08

Telephoned USPTO
again at about 11:30 AM
to inquire about status of
CoL. Promised return
call not received.

Escalated to management
again. Should receive
return call in 48 hours.

8/5/08

Telephoned USPTO
again at about 1:40 PM.
Has been escalated to
management. Nothing
more they can. Will
get call back eventually.

MJR

* 1126590902